

# SR320 THRU SR3200

3.0 A Schottky Barrier Rectifier



## FEATURES

- \* Low forward voltage drop
- \* High current capability
- \* High reliability
- \* Low Power Loss, High Efficiency

## MECHANICAL DATA

- \* Case: Molded plastic
- \* Epoxy: UL 94V-0 rate flame retardant
- \* Lead: Axial leads, solderable per MIL-STD-202, method 208 guaranteed
- \* Polarity: Color band denotes cathode end
- \* Mounting position: Any
- \* Weight: 0.34 grams

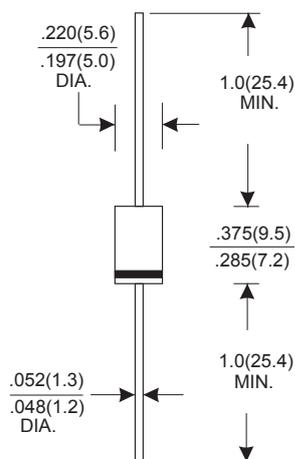
## VOLTAGE RANGE

20 to 200 Volts

## CURRENT

3.0 Ampere

### DO-201AD



Dimensions in inches and (millimeters)

## Maximum Ratings and Electrical Characteristics

- \* Rating at 25 °C ambient temperature unless otherwise specified.
- \* Single phase, half wave, 60 Hz, resistive or inductive load.
- \* For capacitive load, derate current by 20%

Type Number	Symbol	SR 320	SR 330	SR 340	SR 350	SR 360	SR 380	SR 3100	SR 3150	SR 3200	Unit
Maximum Repetitive Peak Reverse Voltage	VRRM	20	30	40	50	60	80	100	150	200	V
Maximum RMS Voltage	VRMS	14	21	28	35	42	56	70	105	140	V
Maximum DC Blocking Voltage	VDC	20	30	40	50	60	80	100	150	200	V
Maximum Average Forward Rectified Current	IF	3.0									A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	IFSM	80									A
Maximum Instantaneous Forward Voltage @3A	VF	0.55		0.70		0.85		0.95			V
Maximum Reverse Current @ Rated VR TA=25 °C TA=125 °C	IR	200 500									uA
Typical Junction Capacitance (Note 1)	Cj	100									pF
Typical Thermal Resistance(Note 2)	RθJA	5									°C/w
Operating and Storage Temperature Range	TJ	-65--+150									°C

NOTE1. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

NOTE2. Leads maintained at ambient temperature at a distance of 9.5mm from the case

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### RATINGS AND CHARACTERISTIC CURVES (SR320 THRU SR3200)

FIG.1-TYPICAL FORWARD CHARACTERISTICS

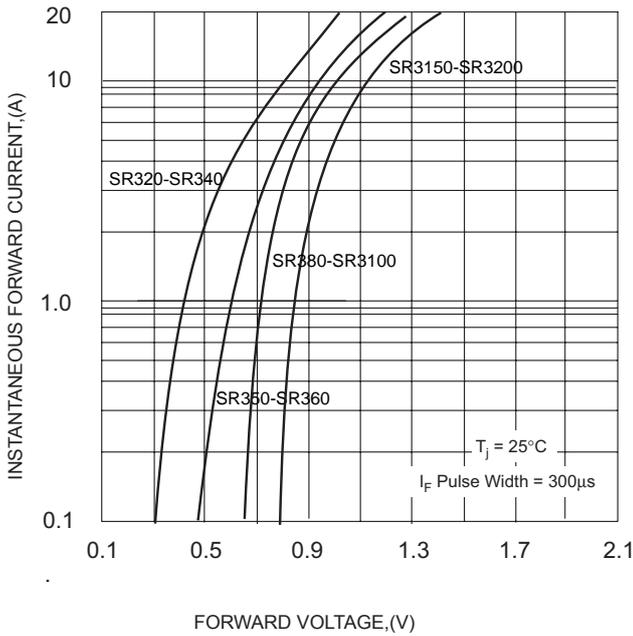


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

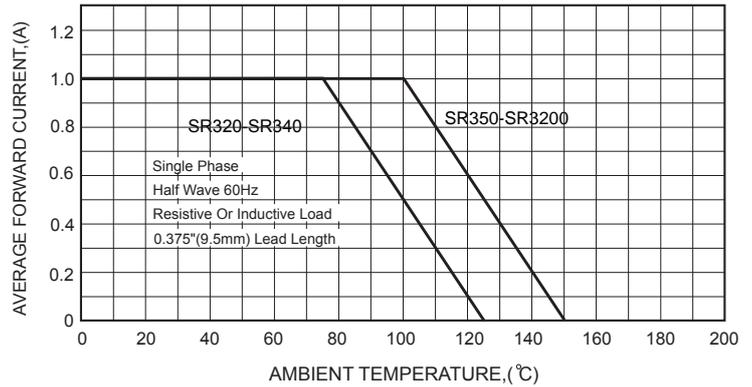


FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

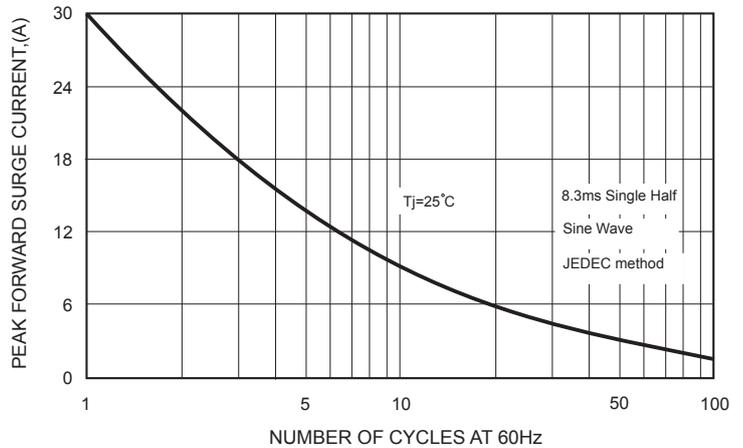


FIG.3 - TYPICAL REVERSE CHARACTERISTICS

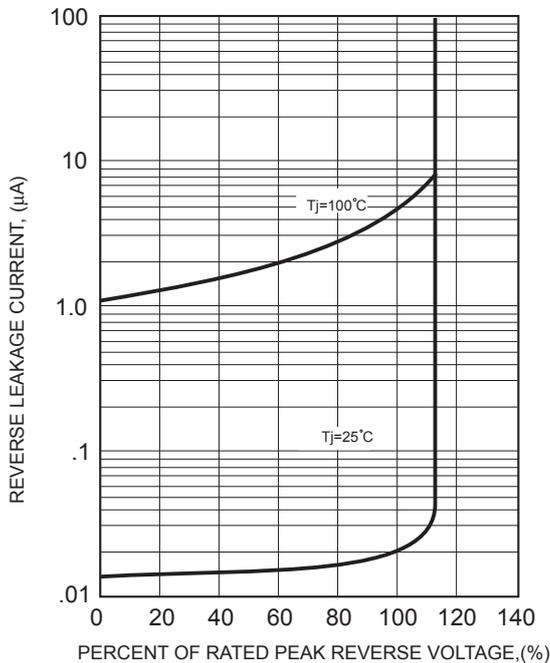


FIG.5-TYPICAL JUNCTION CAPACITANCE

